Claims

What is claimed is:

1. A computer system comprising:

a first video controller;

a second video controller; and

a switching device configured to receive a first signal from the first video controller and a second signal from the second video controller, and configured to provide the first signal or the second signal to a first display device.

2. The computer system of claim 1, further comprising:

a chipset that includes the first video controller.

3. The computer system of claim 2, wherein the second video controller is coupled to the chipset.

4. The computer system of claim 3, wherein the chipset includes an AGP port, and wherein the second video controller is configured to provide the second signal to the switching device using the AGP port.

5. The computer system of claim 3, wherein the chipset includes the switching device.

6. The computer system of claim 2, further comprising:

a processor coupled to the chipset; and

a system memory configured to store a program that is executable by the processor;

wherein the program includes instructions for causing the switching device to provide the first signal or the second signal to the first display device.

7. The computer system of claim 1, wherein the switching device is coupled to a first connector configured to receive the first display device.

8. The computer system of claim 7, wherein the switching device is coupled to a second connector configured to receive a second display device.

9. The computer system of claim 8, wherein the switching device is configured to provide the first signal or the second signal to the second display device.

10. A computer system comprising:

a first video controller;

an interface configured to receive a second video controller; and

a switching device coupled to the first video controller and the interface;

wherein the switching device is configured to provide a first signal from the first video controller to a first display device in response to the second video controller not being coupled to the interface, and wherein the switching device is configured to provide a second signal from the second video controller to the first display device in response to the second video controller being coupled to the interface.

11. The computer system of claim 10, further comprising:

a chipset that includes the first video controller.

12. The computer system of claim 11, wherein the interface is coupled to the chipset.

13. The computer system of claim 12, wherein the chipset includes an AGP port, and wherein the AGP port is configured to receive the second signal from the second video controller.

14. The computer system of claim 12, wherein the chipset includes the switching device.

15. The computer system of claim 11, further comprising:

a processor coupled to the chipset; and

a system memory configured to store a program that is executable by the processor;

wherein the program includes instructions for causing the switching device to provide the first signal from the first video controller to the first display device in response to the second video controller not being coupled to the interface, and wherein the program includes instructions for causing the switching device to provide the second signal from the second video controller to the first display device in response to the second video controller being coupled to the interface.

16. The computer system of claim 10, wherein the switching device is coupled to a first connector configured to receive the first display device.

17. The computer system of claim 16, wherein the switching device is coupled to a second connector configured to receive a second display device.

18. The computer system of claim 17, wherein the switching device is configured to provide the first signal or the second signal to the second display device.

19. A computer program product comprising:

a computer program processable by a computer system for causing the computer system to:

detect the presence of a first video controller;

set a switching device to provide a first signal from the first video controller to a first display device in response to a second video controller not being present; and

set the switching device to provide a second signal from the second video controller to the first display device in response to the second video controller being present; and

a storage apparatus from which the computer program is accessible by the computer system.

20. The computer program product of claim 19, wherein the computer program is processable by the computer system for causing the computer system to:

set the switching device to provide the first signal from the first video controller to a second display device in response to the second video controller and a second display device being present.